

	SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY, BANGALORE - 562 157	RECORD FORMATS (ISO 9001:2008)
	R/PP/04/02: Rev. 02	III SEMESTER A SECTION TIME TABLE WITH EFFECT FROM 18/10/2021

DEPARTMENT: ELECTRICAL AND ELECTRONICS ENGINEERING

SEM: III A SECTION

Room No: E210

SEM: III A SECTION						Room No: E210			
DAY	09.00 AM TO 09.55 AM	09.55 AM TO 10.50 AM	10.50 AM TO 11.00 AM	11.00 AM TO 11.55 AM	11.55 AM TO 12.50 PM	12.50 PM TO 01.35 PM	01.35 PM TO 02.30 PM	02.30 PM TO 03.25 PM	03.25 PM TO 04.20 PM
MON	T&G (NMS)	AEC (VNA)	TEA BREAK	ECA (PJ)	MAT	LUNCH BREAK	DSD (RRK)	AEC (TUT)	LIB
TUE	MAT	DSD (RRK)		AEC (VNA)	EEM (RTM)		EM LAB(B1) /PK,NKR EL LAB (B2) VNA,RTM		
WED	DSD (RRK)	T&G (NMS)		MAT	EEM (RTM)		EM LAB(B2) /NKR,NMS EL LAB (B1) VNA,NBR		
THR	AEC (VNA)	T & G (NMS)		MAT	AEC (VNA)		DSD (RRK)	ECA (PJ)	
FRI	EEM (RTM)	ECA (PJ)		T&G (NMS)	ECA (PJ)		EEM (RTM)	CPC (PBB)	CPC (PBB)
SAT	DIP MATHS			E SHIKSHANA			ECA (TUT)	LG	

SUB CODE	SUBJECT NAME	STAFF NAMES
18MAT31	Transform Calculus	
18EE32	Electric Circuit Analysis	Jyothi P
18EE33	Transformers & Generators	Nanda M Shivamoggi
18EE34	Analog Electronic Circuits	Vidyashree N A
18EE35	Digital System Design	Rekha Radhakrishnan
18EE36	Electrical & Electronic Measurements	Reshma T M
18EEL37	Electrical Machines Lab	
18CPC39	CPC	Prashanth B B
18EEL38	Electronics Lab	
	E SHIKSHANA	Dr. M S SURESH

Prepared by: Dr. H L SURESH

Designation: HOD

PROF. & HEAD
DEPT. OF ELECTRICAL ENGG.
SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY
Krishnadevarayanagar, Hunasamaranahalli
(Via) Yelahanka, Bangalore-562 157

Approved by: Dr. V R Manjunath

Designation: Principal

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BANGALORE - 562 157

R/PP/04/02: Rev. 01

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V SEMESTER A SECTION TIME TABLE
WITH EFFECT FROM 1/10/2021

DEPARTMENT: ELECTRICAL AND ELECTRONICS ENGINEERING

SEM: V A SECTION

Room No: E 303

Room No: E 303									
DAY	09.00 AM TO 09.55 AM	09.55 AM TO 10.50 AM	10.50 AM TO 11.00 AM	11.00 AM TO 11.55 AM	11.55 AM TO 12.50 PM	12.50 PM TO 01.35 PM	01.35 PM TO 02.30 PM	02.30 PM TO 03.25 PM	03.25 PM TO 04.20 PM
MON	PE LAB(A1)/BT,RTM LAB(A2)/RS,RRK				μC PE (RSP)	LUNCH BREAK	S&S (HLS)	μC (TUT)	
TUE	S&S (HLS)	μC (RS)	TEA BREAK	EMD (DB)	HVE (NKR)		μC (RS)	M&E (CVM)	LIB
WED	EMD (DB)	PE (RSP)		HVE (NKR)	μC (RS)		PE LAB(A2) KSR.BT μC LAB(A3)/RRK,RS		
THR	HVE (NKR)	μC (RS)		PE (RSP)	S&S (HLS)		M&E (CVM)	EMD (DB)	EVS
FRI	PE (RSP)	M&E (CVM)		S&S (HLS)	HVE (NKR)		PE LAB(A3) KSR,BT μC LAB(A1)/RRK,NBR		
SAT	M&E (CVM)	EMD (DB)		PE (TUT)	LG		E SHIKSHANA		
SUB CODE					SUBJECT NAME				

18EE51 Management & Entrepreneurship

18EE52 Microcontroller

18EE53 Power Electronics

18EE54 Signals & Systems

18EE55 Electrical Machine Design

18EE56 High Voltage Engineering

18EEL57 Microcontroller Lab

18EEL58 Power Electronics Lab

18CIV59 Environmental Studies

E SHIKSHANA

Dr. C V Mohan

R Subha

Dr. R Sivapriyan

Dr. H L Suresh

D Beula

Nitin Kumar Reddy K N

Vyshnavi D R

Dr. R SUBHA

Prepared by: Dr. H L SURESH

Designation: HOD

Signature:

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Mysr Velabanka, Bangalore-562 157

Approved by: Dr. V R Manjunath

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	SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY, BANGALORE - 562 157	RECORD FORMATS (ISO 9001:2008)
	R/PP/04/02: Rev. 01	V SEMESTER B SECTION TIME TABLE WITH EFFECT FROM 1/10/2021

DEPARTMENT: ELECTRICAL AND ELECTRONICS ENGINEERING

SEM: V B SECTION

Room No: E 304

DAY	09.00 AM TO 09.55 AM	09.55 AM TO 10.50 AM	10.50 AM TO 11.00 AM	11.00 AM TO 11.55 AM	11.55 AM TO 12.50 PM	12.50 PM TO 01.35 PM	01.35 PM TO 02.30 PM	02.30 PM TO 03.25 PM	03.25 PM TO 04.20 PM
MON	M&E (AC)	S&S (NBR)	TEA BREAK	M&E (AC)	EMD (KSR)	LUNCH BREAK	PE LAB(B1)/BT, KSR, μC LAB(B2)RS,GKB		
TUE	HVE (MRS)	PE (BT)		M&E (AC)	μC (RRK)		PE LAB(B2)/BT, KSR, μC LAB(B3)RRK,NBR		
WED	S&S (NBR)	EMD (KSR)		μC (RRK)	PE (BT)		EVS	PE (TUT)	LIB
THR	PE LAB(B3)/RTM, KSR, μC LAB(B1)RRK,GKB				HVE (MRS)		S&S (NBR)	PE (BT)	LG
FRI	EMD (KSR)	μC (RRK)	TEA BREAK	PE (BT)	HVE (MRS)		M&E (AC)	μC (TUT)	
SAT	μC (RRK)	S&S (NBR)		EMD (KSR)	HVE (MRS)		E SHIKSHANA		

SUB CODE	SUBJECT NAME	STAFF NAMES
18EE51	Management & Entrepreneurship	Anchal Chabrra
18EE52	Microcontroller	Rekha Radhakrishnan
18EE53	Power Electronics	Bindiya Tyagi
18EE54	Signals & Systems	Nayana B R
18EE55	Electrical Machine Design	Kumaraswamy R
18EE56	High Voltage Engineering	Sidhappaji M R
18EEL57	Microcontroller Lab	
18EEL58	Power Electronics Lab	
18 CIV 59	Environmental Studies	Vishnavi D.R.
	E SHIKSHANA	Dr.R Sivapriyan

Prepared by: Dr. H L SURESH
 Designation: HOD
 Signature: *[Signature]*
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 Krishnadevarayanagar, Hunasamaranahalli

Approved by: Dr. V R Manjunath
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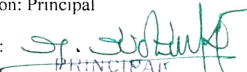
VII SEMESTER A SECTION TIME TABLE
WITH EFFECT FROM 01/10/2021

DEPARTMENT: ELECTRICAL AND ELECTRONICS ENGINEERING

SEM: VII A SECTION			Room No: E 203		Elective Rooms: MNSST(E006), AI (006), SG (E 212)					
DAY	09.00 AM TO 09.55 AM	09.55 AM TO 10.50 AM	10.50 AM TO 11.00 AM	11.00 AM TO 11.55 AM	11.55 AM TO 12.50 PM	12.50 PM TO 01.35 PM	01.35 PM TO 02.30 PM	02.30 PM TO 03.25 PM	03.25 PM TO 04.20 PM	
MON	HV LAB (A1)/CVM,MRS PSSLAB(A2)/PS,DB					EECA (CVM)	LUNCH BREAK	PSA- 2 (MK)	LG	
TUE	AI/UEP/SG	SWE/MNSS T	TEA BREAK	PSP (BT)	EECA (CVM)	HV LAB(A2)/BC,RSP PSSLAB(A3)/PJ,MSS				
WED	SWE/MNSS T	PSP (BT)		AI/UEP/SG	EECA (CVM)	E SHIKSHANA				
THR	PSA- 2 (MK)	PSA-2 (MK)		PSP (BT)	EECA (CVM)	PROJECT WORK (HLS, MSS)				
FRI	PSP (BT)	SWE/MNSS T		PSA- 2 (MK)	AI/UEP/SG	HV LAB(A3)/RSP,MRS PSSLAB(A1)/PS,PJ				
SAT	SWE/MNSS T	AI/UEP/ SG		LIB		PROJECT WORK (HLS, MSS)				
SUB CODE		SUBJECT NAME				STAFF NAMES				
18EE71	Power System Analysis-2					Dr. Mahesh K				
18EE72	Power System Protection					Bindiya Tyagi				
18EE73X	Solar and Wind Energy/Micro and nano scale sensors & tr					Nitin Kumar Reddy/ Anchal Chhabra				
18EE74X	Utilization Of Electrical Power/Smart Grid/AI Techniques					Reshma TM/Sumalatha P S/ Dr R Sivapriyan				
18XX74X	Electrical Energy Conservation & Auditing					Dr. C V Mohan				
18EEL76	Power System Simulation Lab									
18EEL77	Relay & High Volatge Engineering									
15EEP78	Project Phase I					Dr. A. L. Suresh, Dr. M. S. Suresh				
Prepared by: Dr. H L SURESH						PRINCIPAL				
Designation: HOD						Krishnadevarayanagar, Hunasamaranahalli, Mangalore Airport Road, BANGALORE				
Signature: SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY Krishnadevarayanagar, Hunasamaranahalli (Via) Yelahanka, Bangalore-562 167						Approved by: Dr. V R Manjunath Designation: Principal Signature: <i>[Signature]</i>				

	SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY, BANGALORE - 562 157	RECORD FORMATS (ISO 9001:2008)
	R/PP/04/02: Rev. 01	VII SEMESTER B SECTION TIME TABLE WITH EFFECT FROM 1/10/2021

DEPARTMENT: ELECTRICAL AND ELECTRONICS ENGINEERING

SEM: VII B SECTION			Room No: E305	Elective Rooms: MNSST(E006), AI (006), SG (E 212)					
DAY	09.00 AM TO 09.55 AM	09.55 AM TO 10.50 AM	10.50 AM TO 11.00 AM	11.00 AM TO 11.55 AM	11.55 AM TO 12.50 PM	12.50 PM TO 01.35 PM	01.35 PM TO 02.30 PM	02.30 PM TO 03.25 PM	03.25 PM TO 04.20 PM
MON	PSP (GKB)	PSA -2 (MSS)	TEA BREAK	PSP (GKB)	EECA (CVM)		HV LAB(B1)/RSP,MRS PSS LAB(B2)/PS,DB		
TUE	AI/UEP/SG	SWE/MNSS T		PSA -2 (MSS)	EECA (CVM)		E SHIKSHANA		
WED	SWE/MNSS T	PSA -2 (MSS)		AI/UEP/SG	EECA (CVM)		HV LAB(B2)/CVM,MRS PSS LAB(B3)/PS,DB		
THR	HV LAB(B3)/CVM,BC PSS LAB(B1)/PS,PJ				EECA (CVM)		PROJECT WORK (MK, RSP)		
FRI	PSP (GKB)	SWE/MNSS T	TEA BREAK	PSA -2 (MSS)	AI/UEP/SG		LIB	LG	
SAT	SWE/MN SST	AI/UEP/ SG1		PSP (GKB)			PROJECT WORK (MK, RSP)		
SUB CODE		SUBJECT NAME				STAFF NAMES			
18EE71		Power System Analysis-2				Dr. M S Suresh			
18EE72		Power System Protection				Girish Kumar B			
18EE73X		Solar and Wind Energy/Micro and nano scale sensors & tr				M R Siddhapaji / Anchal Chhabra			
18EE74X		Utilization Of Electrical Power/Smart Grid/AI Techniques				Vidyashree N A/Sumalatha P S/ Dr R Sivapriyan			
18XX74X		Electrical Energy Conservation & Auditing				Dr. C V Mohan			
18EEL76		Power System Simulation Lab							
18EEL77		Relay & High Volatge Engineering							
18EEP78		Project Phase I				Dr. Mahesh K, Dr. R. Sivapriyan			
Prepared by: Dr. H L SURESH Designation: HOD						Approved by: Dr. V R Manjunath Designation: Principal			
Signature: 						Signature: 			
DEPT. OF ELECTRICAL ENGG. JSS V. VISVESVARAYA INSTITUTE OF TECHNOLOGY						PRINCIPAL			

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SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY,

R/PP/04/02: Rev. 01

RECORD FORMATS

VII SEMESTER B SECTION TIME TABLE
WITH EFFECT FROM 01/10/2021

DEPARTMENT: ELECTRICAL AND ELECTRONICS ENGINEERING

SEM: VII PARALLEL COURSE

Room No: E305

Elective Rooms: T&C (E006)

DAY	09.00 AM TO 09.55 AM	09.55 AM TO 10.50 AM	10.50 AM TO 11.00 AM	11.00 AM TO 11.55 AM	11.55 AM TO 12.50 PM	12.50 PM TO 01.35 PM	01.35 PM TO 02.30 PM	02.30 PM TO 03.25 PM	03.25 PM TO 04.20 PM
MON	PSP (GKB)	PSA -2 (MSS)	TEA BREAK	PSP (GKB)	TC (RS)		HV LAB(B1)/RSP,MRS PSS LAB(B2)/PS,DB		
TUE	UEP	HVE (BC)		PSA -2 (MSS)	HVE (BC)				
WED	TC (RS)	PSA -2 (MSS)		UEP	HVE (BC)		HV LAB(B2)/CVM,MRS PSS LAB(B3)/PS,DB		
THR	HV LAB(B3)/CVM,BC PSS LAB(B1)/PS,PJ						TC (RS)		
FRI	PSP (GKB)	TC (RS)	TEA BREAK	PSA -2 (MSS)	UEP				
SAT	PSP (GKB)	HVE (BC)		UEP			PROJECT WORK (CHK, RSP)		
SUB CODE	SUBJECT NAME								
17EE71	Power System Analysis-2				Dr. M S Suresh				
17EE72	Power System Protection				Girish Kumar B				
17EE73	High Volatge Engineering				Bhaskar C				
17EE74X	Utilization Of Electrical Powers				Vidyashree N A				
17EE75Y	Testing and commissioning of power system apparatus				Dr.R Subha				
17EEL76	Power System Simulation Lab								
17EEL77	Relay & High Volatge Engineering								
17EEP78	Project Phase I				Dr. Mahesh. K , Dr. R. Srinapriyan				

Prepared by: Dr. H L SURESH

Designation: HOD

Signature:

DEPT. OF ELECTRICAL ENGG.

Approved by: Dr. V R Manjunath

Designation: Principal

Signature:

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(via) Yelahanka Bangalore-562 167SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY
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	SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY, BANGALORE - 562 157	RECORD FORMATS	(ISO 9001:2008)
R/PP/04/02: Rev. 02		III SEMESTER B SECTION TIME TABLE FROM 18/10/2021	WITH EFFECT

DEPARTMENT: ELECTRICAL AND ELECTRONICS ENGINEERING

SEM: III B SECTION

Room No: 211

SEM: III B SECTION									
DAY	09.00 AM TO 09.55 AM	09.55 AM TO 10.50 AM	10.50 AM TO 11.00 AM	11.00 AM TO 11.55 AM	11.55 AM TO 12.50 PM	12.50 PM TO 01.35 PM	01.35 PM TO 02.30 PM	02.30 PM TO 03.25 PM	03.25 PM TO 04.20 PM
MON	T&G (PK)	EEM (BC)	TEA BREAK	MAT	EEM (BC)	LUNCH BREAK	EM LAB(B1) /NKR,NMS EL LAB (B2) VNA,RTM		
TUE	AEC (DB)	MAT		T&G (PK)	ECA (PS)		AEC (DB)	ECA (PS)	ECA (TUT)
WED	ECA (PS)	MAT		DSD (GKB)	AEC (DB)		EEM (BC)	LG	
THR	T&G (PK)	MAT		T&G (PK)	DSD (GKB)		EM LAB(B2) /PK,NKR EL LAB (B1) /VNA,RTM		
FRI	AEC (DB)	ECA (PS)		DSD (GKB)	EEM (BC)		DSD (GKB)	CPC (PBB)	CPC (PBB)
SAT	DIP MATHS			AEC (TUT)	LIB		E SHIKSHANA		
SUB CODE	SUBJECT NAME				STAFF NAMES				
18MAT31	Transform Calculus								
18EE32	Electric Circuit Analysis				P Sumalata				
18EE33	Transformers & Generators				Pradeep Kumar K				
18EE34	Analog Electronic Circuits				D Beula				
18EE35	Digital System Design				Girish Kumar B				
18EE36	Electrical & Electronic Measurements				Bhaskar C				
18EEL37	Electrical Machines Lab								
18EEL38	Electronics Lab								
18CPC39	CPC				Prashanth B B				
	E SHIKSHANA				D Beula				
Prepared by: Dr. H L SURESH						Approved by: Dr. V R MANJUNATH			
Designation: HOD						Designation: Principal			
Signature:						Signature:			