

Week - 1

SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY, BANGALORE - 562 157		RECORD FORMATS (ISO 9001:2008)
R/PP/04/02: Rev. 01		V SEMESTER A SECTION OFF-LINE CLASS TIME TABLE WITH EFFECT FROM 30/11/2020 to 6/12/2020
DEPARTMENT: ELECTRICAL AND ELECTRONICS ENGINEERING		
SEM: V A SECTION		

OFF LINE PRACTICAL CLASS

DAY	09.00 AM TO 09.55 AM	09.55 AM TO 10.50 AM	10.50 AM TO 11.00 AM	11.00 AM TO 11.55 AM	11.55 AM TO 12.50 PM	12.50 PM TO 01.35 PM	01.35 PM TO 02.30 PM	02.30 PM TO 03.25 PM	03.25 PM TO 04.20 PM
MON	Power Electronics Laboratory(KSR) B1-V-A					Lunch Break	Power Electronics Laboratory(VNA) B3-V-A		
TUE	Power Electronics Laboratory(RTM) B2-V-A						Micro Controller Laboratory(Dr.RS) B1 V-A		
WED	Micro Controller Laboratory(NBR) B3 V-A						Micro Controller Laboratory(RRK) B2 V-A		

ON-LINE CLASS

DAY	09.30 AM TO 10:30 AM	11:00 AM TO 12.00 PM	01:00 PM TO 02:00PM	02:30 PM TO 03.30 PM
THR	EMD(DB)	PE(BT)	S&S(HLS)	HVE(CVM)
FRI	S&S(HLS)	EMD(DB)	MP(RS)	M&E(AC)
SAT	HVE(CVM)	S&S(HLS)	EMD(DB)	PE(BT)

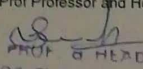
18EE51	Management and Entrepreneurship(M&E)	18EE56	High Voltage Engineering(HVE)
18EE52	Micro Controller(MP)	18EEL57	Micro Controller Laboratory
18EE53	Power Electronics(PE)	18EEL58	Power Electronics Laboratory
18EE54	Signal and Systems(S&S)	18CIV59	Environmental Studies
18EE55	Electrical Machine Design(EMD)		

STAFF NAMES

AC	Mrs. Anchal	18EE51	Dr. H L Suresh	HLS	18EE54
RS	Dr. R Subha	18EE52	Mrs D Beula	DB	18EE55
BT	Mrs. Bindiya Tayagi	18EE53	Dr. C V Mohan	CVM	18EE56

Prepared by: Dr H.L Suresh

Designation: Prof Professor and Hod , EEE Department

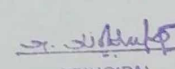
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DEPT OF ELECTRIC L ENGG

SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY
Krishnadevarayanagar, Hunasamaranahalli
Vijay Yelahanka Bangalore-562 157

Approved by: Dr. V R Manjunath

Designation: Principal

Signature: 

PRINCIPAL

SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY
Krishnadevarayanagar, Hunasamaranahalli
Vijay Yelahanka Bangalore-562 157

SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY, BANGALORE - 562 157		RECORD FORMATS (ISO 9001:2008)
R/PP/04/02: Rev. 01		V SEMESTER A SECTION OFF-LINE CLASS TIME TABLE WITH EFFECT FROM 7/12/2020 to 12/12/2020
DEPARTMENT: ELECTRICAL AND ELECTRONICS ENGINEERING		
SEM: V A SECTION		

ON LINE CLASS

DAY	09.30 AM TO 10.30 AM	11.00 AM TO 12.00 PM	01.00 PM TO 02.00 PM	02.30 PM TO 03.30 PM
MON	MP(RS)	M&E(AC)	PE(BT)	EMD(DB)
TUE	M&E(AC)	MP(RS)	HVE(CVM)	S&S(HLS)
WED	PE(BT)	HVE(CVM)	M&E(AC)	MP(RS)

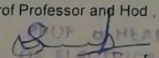
OFF LINE PRACTICAL CLASS

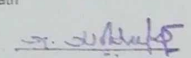
DAY	09.00 AM TO 09.55 AM	09.55 AM TO 10.50 AM	10.50 AM TO 11.00 AM	11.00 AM TO 11.55 AM	11.55 AM TO 12.50 PM	12.50 PM TO 01.35 PM	01.35 PM TO 02.30 PM	02.30 PM TO 03.25 PM	03.25 PM TO 04.20 PM
THR	Micro Controller Laboratory(RRK) B2 V-A					Lunch Break	Power Electronics Laboratory(KSR) B1-V-A		
FRI	Micro Controller Laboratory(Dr RS) B1 V-A						Power Electronics Laboratory(Dr. MK, PJ) B3-V-A		
SAT	Micro Controller Laboratory(RRK) B3 V-A						Power Electronics Laboratory(KSR) B2-V-A		

18EE51	Management and Entrepreneurship(M&E)	18EE56	High Voltage Engineering(HVE)
18EE52	Micro Controller(MP)	18EEL57	Micro Controller Laboratory
18EE53	Power Electronics(PE)	18EEL58	Power Electronics Laboratory
18EE54	Signal and Systems(S&S)	18CIV59	Environmental Studies
18EE55	Electrical Machine Design(EMD)		

STAFF NAMES

AC	Mrs. Anchal	18EE51	Dr. H L Suresh	HLS	18EE54
RS	Dr. R Subha	18EE52	Mrs D Beula	DB	18EE55
BT	Mrs. Bindya Tayagi	18EE53	Dr. C V Mohan	CVM	18EE56

Prepared by: Dr H L Suresh
Designation: Prof Professor and Hod, EEE Department
Signature: 

Approved by: Dr. V R Manjunath
Designation: Principal
Signature: 

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SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY
Krishnadevarayanagar, Hunsamaranahalli
97th Yelahanka, Bangalore-562 157

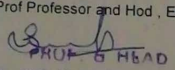
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SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY
Krishnadevarayanagar, Hunsamaranahalli
International Airport Road, Bangalore-562 157

Week-1

SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY, BANGALORE - 562 157		RECORD FORMATS (ISO 9001:2008)							
R/PP/04/02: Rev. 01		V SEMESTER B SECTION OFF-LINE CLASS TIME TABLE WITH EFFECT FROM 30/11/2020 to 6/12/2020							
DEPARTMENT: ELECTRICAL AND ELECTRONICS ENGINEERING									
SEM: V B SECTION									
OFF LINE PRACTICAL CLASS									
DAY	09.00 AM TO 09.55 AM	09.55 AM TO 10.50 AM	10.50 AM TO 11.00 AM	11.00 AM TO 11.55 AM	11.55 AM TO 12.50 PM	12.50 PM TO 01.35 PM	01.35 PM TO 02.30 PM	02.30 PM TO 03.25 PM	03.25 PM TO 04.20 PM
MON	Micro Controller Laboratory(MRS) V-B, B1					Lunch Break	Micro Controller Laboratory(Dr.NBR) B3		
TUE	Micro Controller Laboratory(Dr.NBR) V-B, B2						Power Electronics Laboratory(VNA) V-B, B1		
WED	Power Electronics Laboratory(RTM) V-B, B3						Power Electronics Laboratory (KSR) V-B, B2		
ON-LINE CLASS									
DAY	09.30 AM TO 10.30 AM		11.00 AM TO 12.00 PM		01:00 PM TO 02:00PM		02:30 PM TO 03.30 PM		
THR	EMD(RSP)		S&S(RS)		MP(RRK)		PE(Dr.NBR)		
FRI	PE(Dr.NBR)		MP(RRK)		EMD(RSP)		M&E(NKR)		
SAT	M&E(NKR)		PE(Dr.NBR)		S&S(RS)		HVE(BC)		
18EE51	Management and Entrepreneurship(M&E)				18EE56	High Voltage Engineering(HVE)			
18EE52	Micro Controller(MP)				18EEL57	Micro Controller Laboratory			
18EE53	Power Electronics(PE)				18EEL58	Power Electronics Laboratory			
18EE54	Signal and Systems(S&S)				18CIV59	Environmental Studies			
18EE55	Electrical Machine Design(EMD)								
STAFF NAMES									
NKR	Mr Nitin Kumar Reddy		18EE51	Dr. Subha R		RS	18EE54		
RRK	Rekha Radhakrishnan		18EE52	Mr. Sivapriyan R		RSP	18EE55		
NBR	Dr.Nayana B R		18EE53	Mr. Bhaskar C		BC	18EE56		

Prepared by: Dr H L Suresh

Designation: Prof Professor and Hod , EEE Department

Signature: 

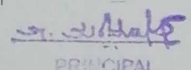
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SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY
Krishnadevarayanagar, Hunasamaranahalli

(Via) Yelahanka Bangalore-562 157

Approved by: Dr. V R Manjunath

Designation: Principal

Signature: 

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SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY
Krishnadevarayanagar, Hunasamaranahalli
Mysore Airport Road, Bangalore-562 157



SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY, BANGALORE - 562 157

RECORD FORMATS (ISO 9001:2008)

R/PP/04/02: Rev. 01

V SEMESTER B SECTION OFF-LINE CLASS TIME TABLE WITH
EFFECT FROM 7/12/2020 to 12/12/2020

DEPARTMENT: ELECTRICAL AND ELECTRONICS ENGINEERING

SEM: V B SECTION

ON LINE CLASS

DAY	09.30 AM TO 10.30 AM	11.00 AM TO 12.00 PM	01.00 PM TO 02.00 PM	02.30 PM TO 03.30 PM
MON	MP(RRK)	HVE(BC)	M&E(NKR)	S&S(RS)
TUE	HVE(BC)	EMD(RSP)	PE(MPV)	M&E(NKR)
WED	S&S(RS)	MP(RRK)	HVE(BC)	EMD(RSP)

OFF LINE PRACTICAL CLASS

OFF LINE PRACTICAL CLASS									
DAY	09.00 AM TO 09.55 AM	09.55 AM TO 10.50 AM	10.50 AM TO 11.00 AM	11.00 AM TO 11.55 AM	11.55 AM TO 12.50 PM	12.50 PM TO 01.35 PM	01.35 PM TO 02.30 PM	02.30 PM TO 03.25 PM	03.25 PM TO 04.20 PM
THR	Power Electronics Laboratory(Dr. MK, PJ) V-B , B3					Lunch Break	Micro Controller Laboratory(Dr.RS) V-B, B3		
FRI	Power Electronics Laboratory(VNA)V-B B2						Micro Controller Laboratory(RRK) V-B, B2		
SAT	Power Electronics Laboratory(RTM) V-B B1						Micro Controller Laboratory(MRS) V-B, B1		

18EE51	Management and Entrepreneurship(M&E)	18EE56	High Voltage Engineering(HVE)
18EE52	Micro Controller(MP)	18EEL57	Micro Controller Laboratory
18EE53	Power Electronics(PE)	18EEL58	Power Electronics Laboratory
18EE54	Signal and Systems(S&S)	18CIV59	Environmental Studies
18EE55	Electrical Machine Design(EMD)		

STAFF NAMES

NKR	Mr Nitin Kumar Reddy	18EE51	Dr. Subha R	RS	18EE54
RRK	Rekha Radhakrishnan	18EE52	Mr. Sivapriyan R	RSP	18EE55
NBR	Dr.Nayana B R	18EE53	Mr. Bhaskar C	BC	18EE56

Prepared by: Dr H.L. Suresh

Designation: Prof Professor and Hod, EEE Department

Signature:

Approved by: Dr. V R Manjunath

Designation: Principal

Signature:

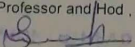
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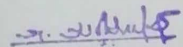
DEPT. OF ELECTRICAL & ELECTRONICS
SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY
Krishnadevarayanagar, Hunasamaranahalli
(Via) Yalahanka Bangalore-562 157

SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY
Krishnadevarayanagar, Hunasamaranahalli
International Airport Road, Bangalore-562 157

WEEK-1

	SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY, BANGALORE - 562 157			RECORD FORMATS (ISO 9001:2008)						
R/PP/04/02: Rev. 01			III SEMESTER A SECTION OFF-LINE CLASS TIME TABLE WITH EFFECT FROM 30/11/2020 to 06/12/2020							
DEPARTMENT: ELECTRICAL AND ELECTRONICS ENGINEERING										
SEM: IIIA SECTION		ON-LINE CLASS								
DAY	09.30 AM TO 10.30 AM	11.00 AM TO 12.00 PM	01.00 PM TO 02.00PM	02.30 PM TO 03.30 PM	04.00 PM TO 05.00PM					
MON	Math-III	EEM(JBB)	DSD(RRK)	T&G(NMS)						
TUE	ECA (MSS)	DSD(RRK)	Math-III	AEC(KSR)						
WED	T&G(NMS)	AEC(KSR)	EEM(JBB)	ECA (MSS)	Professional Ethics and Cyber Law					
OFF LINE PRACTICAL CLASS										
DAY	09.00 AM TO 09.55 AM	09.55 AM TO 10.50 AM	10.50 AM TO 11.00 AM	11.00 AM TO 11.55 AM	11.55 AM TO 12.50 PM	12.50 PM TO 01.35 PM	01.35 PM TO 02.30 PM	02.30 PM TO 03.25 PM	03.25 PM TO 04.20 PM	
THR	Electronics Laboratory B3 (Dr. NBR)) III-A					LUNCH BREAK	Electrical Machines Laboratory - 1(NMS) B1 III-A			
FRI	Electronics Laboratory B1 (KBV) III-A						Electrical Machines Laboratory - 1(NMS) B2 III-A			
SAT	Electrical Machines Laboratory - 1(PJ) B3 III-A						Electronics Laboratory B2 (VNA) III-A			
18MAT31	Engineering Mathematics -III		18EE35		Digital System Design(DSD)					
18EE32	Electrical Circuit Analysis(ECA)		18EE36		Electrical and Electronics Measurements(EEM)					
18EE33	Transformers and Generators(T&G)		18EEL37		Electrical Machines Laboratory - 1					
18EE34	Analog Electronics circuits(AEC)		18EEL38		Electronics Laboratory					
18CPC39	Constitution of India, Professional Ethics and Cyber Law									
STAFF NAMES										
		18MAT31	Mr. Kumar Swamy	KSR	18EE34					
MSS	Dr. M S Suresh	18EE32	Mrs.Rekha Radhkrishnan	RRK	18EE35					
NMS	Mrs. Nanda Shivmogi	18EE33	Mrs. Janki B Borad	JBB	18EE36					
PBB	Mr. Prashanth B B									
					18CPC39					

Prepared by: Dr H L Suresh
Designation: Prof Professor and Hod , EEE Department
Signature: 

Approved by: Dr. V R Manjunath
Designation: Principal
Signature: 

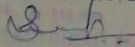
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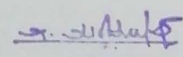
DEPT. OF ELECTRICAL ENG
SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY
Krishnadevarayanagar, Hunsuramranahalli
(Via) Telephone: Bangalore-562 157

SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY
Krishnadevarayanagar, Hunsuramranahalli
International Airport Road, Bangalore-562 157

WEEK - 2

	SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY, BANGALORE - 562 157	RECORD FORMATS (ISO 9001:2008)							
R/PP/04/02: Rev. 01		III SEMESTER A SECTION OFF-LINE CLASS TIME TABLE WITH EFFECT FROM 7/12/2020 to 12/12/2020							
DEPARTMENT: ELECTRICAL AND ELECTRONICS ENGINEERING									
SEM IIIA SECTION									
OFF LINE PRACTICAL CLASS									
DAY	09.00 AM TO 09.55 AM	09.55 AM TO 10.50 AM	10.50 AM TO 11.00 AM	11.00 AM TO 11.55 AM	11.55 AM TO 12.50 PM	12.50 PM TO 01.35 PM	01.35 PM TO 02.30 PM	02.30 PM TO 03.25 PM	03.25 PM TO 04.20 PM
MON	Electrical Machines Laboratory - 1 (NMS) B1 III-A					Lunch Break	Electronics Laboratory B1 (KBV) III-A		
TUE	Electronics Laboratory B3 (Dr. NBR) III-A						Electronics Laboratory B2 (DB) III-A		
WED	Electrical Machines Laboratory - 1(BC) B2 III-A						Electrical Machines Laboratory - 1(PJ) B3 III-A		
ON LINE CLASS									
DAY	09.30 AM TO 10.30 AM		11.00 AM TO 12.00 PM		01.00 PM TO 02:00PM		02:30 PM TO 03.30 PM		04:00 PM TO 05:00PM
THR	AEC(KSR)		T&G(NMS)		ECA (MSS)		Math-III		
FRI	DSD(RRK)		ECA (MSS)		AEC(KSR)		EEM(JBB)		
SAT	EEM(JBB)		Math-III		T&G(NMS)		DSD(RRK)		
18MAT31	Engineering Mathematics -III				18EE35	Digital System Design(DSD)			
18EE32	Electrical Circuit Analysis(ECA)				18EE36	Electrical and Electronics Measurements(EEM)			
18EE33	Transformers and Generators(T&G)				18EEL37	Electrical Machines Laboratory - 1			
18EE34	Analog Electronics circuits(AEC)				18EEL38	Electronics Laboratory			
18CPC39	Constitution of India, Professional Ethics and Cyber Law								
STAFF NAMES									
		18MAT31	Mr. Kumar Swamy	KSR	18EE34				
MSS	Dr. M S Suresh	18EE32	Mrs. Rekha Radhkrishnan	RRK	18EE35				
NMS	Mrs. Nanda Shivmogi	18EE33	Mrs. Janki B Borad	JBB	18EE36				
PBB	Mr. Prashanth B B	18CPC39							

Prepared by: Dr. H L Suresh
Designation: Prof. Professor and Mod, EEE Department
Signature: 

Approved by: Dr. V R Manjunath
Designation: Principal
Signature: 

PRINCIPAL
SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY
Hunaseemaranahalli
International Airport Road, Bangalore-562 157



SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY, BANGALORE - 562 157

RECORD FORMATS (ISO 9001:2008)

R/PP/04/02: Rev. 01

III SEMESTER B SECTION OFF-LINE CLASS TIME TABLE WITH EFFECT FROM 30/11/2020 to 6/12/2020

DEPARTMENT: ELECTRICAL AND ELECTRONICS ENGINEERING

SEM III-B SECTION

ON LINE CLASS TT

DAY	09.30 AM TO 10.30 AM	11.00 AM TO 12.00 PM	01.00 PM TO 02.00 PM	02.30 PM TO 03.30 PM	04.00 PM TO 05.00 PM
MON	Math-III	EEM(RTM)	DSD(PS)	T&G(MRS)	
TUE	EEM(RTM)	DSD(PS)	Math-III	AEC(VNA)	Constitution of India, Professional Ethics and Cyber Law
WED	ECA(PJ)	AEC(VNA)	EEM(RTM)	T&G(MRS)	

OFF LINE PRACTICAL CLASS

DAY	09.00 AM TO 09.55 AM	09.55 AM TO 10.50 AM	10.50 AM TO 11.00 AM	11.00 AM TO 11.55 AM	11.55 AM TO 12.50 PM	12.50 PM TO 01.35 PM	01.35 PM TO 02.30 PM	02.30 PM TO 03.25 PM	03.25 PM TO 04.20 PM
THR	Electrical Machines Laboratory - 1(PJ) B3 III-B					Lunch Break	Electronics Laboratory B2 (KBV) III-B		
FRI	Electrical Machines Laboratory - 1(BC)B1 III-B						Electronics Laboratory B3 (DB) III-B		
SAT	Electronics Laboratory B3 (VNA) III-B						Electrical Machines Laboratory - 1(NKR) B2 III-B		

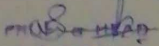
18MAT31	Engineering Mathematics -III	18EE35	Digital System Design(DSD)
18EE32	Electrical Circuit Analysis(ECA)	18EE36	Electrical and Electronics Measurements(EEM)
18EE33	Transformers and Generators(T&G)	18EEL37	Electrical Machines Laboratory - 1
18EE34	Analog Electronics circuits(AEC)	18EEL38	Electronics Laboratory
18CPC39	Constitution of India, Professional Ethics and Cyber Law		

STAFF NAMES

		18MAT31	Mr. Vidyashree N A	VNA	18EE34
PJ	Ms. P Jyothi	18EE32	Mrs. P Sumalatha	PS	18EE35
MRS	Mr. M R Sidappaji	18EE33	Mrs. Reshma T M	RTM	18EE36
PBB	Mr. Prashanth B B				18CPC39

Prepared by: Dr H L Suresh

Designation: Prof Professor and Hod, EEE Department

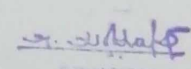
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DEPT. OF ELECTRICAL ENGG

SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY
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(Via) Yelahanka Bangalore-562 157

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Designation: Principal

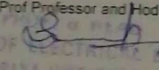
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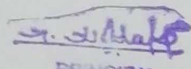
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WEEK - 2

	SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY, BANGALORE - 562 157				RECORD FORMATS (ISO 9001:2008)				
	R/PP/04/02: Rev. 01				III SEMESTER B SECTION OFF-LINE CLASS TIME TABLE WITH EFFECT FROM 7/11/2020 to 12/12/2020				
DEPARTMENT: ELECTRICAL AND ELECTRONICS ENGINEERING									
SEM: III-B SECTION									
OFFLINE PRACTICAL CLASS									
DAY	09.00 AM TO 09.55 AM	09.55 AM TO 10.50 AM	10.50 AM TO 11.00 AM	11.00 AM TO 11.55 AM	11.55 AM TO 12.50 PM	12.50 PM TO 01.35 PM	01.35 PM TO 02.30 PM	02.30 PM TO 03.25 PM	03.25 PM TO 04.20 PM
MON	Electronics Laboratory B3 (VNA) III-B						Electrical Machines Laboratory - 1(PJ) B3 III-B		
TUE	Electrical Machines Laboratory - 1(NMS)B1 III-B						Electrical Machines Laboratory - 1(JBB) B2 III-B		
WED	Electronics Laboratory B2 (KBV) III-B						Electronics Laboratory B3 (VNA) III-B		
ONLINE CLASS									
DAY	09.30 AM TO 10.30 AM		11.00 AM TO 12.00 PM		01.00 PM TO 02.00 PM		02.30 PM TO 03.30 PM		04.00 PM TO 05.00 PM
THR	AEC(VNA)		T&G(MRS)		ECA(PJ)		Math-III		
FRI	DSD(PS)		ECA(PJ)		AEC(VNA)		EEM(RTM)		
SAT	T&G(MRS)		Math-III		ECA(PJ)		DSD(PS)		
18MAT31	Engineering Mathematics -III				18EE35	Digital System Design(DSD)			
18EE32	Electrical Circuit Analysis(ECA)				18EE36	Electrical and Electronics Measurements(EEM)			
18EE33	Transformers and Generators(T&G)				18EEL37	Electrical Machines Laboratory - 1			
18EE34	Analog Electronics circuits(AEC)				18EEL38	Electronics Laboratory			
18CPC39	Constitution of India, Professional Ethics and Cyber Law								
STAFF NAMES									
		18MAT31	Mr. Vidyashree N A		VNA	18EE34			
	Ms. P Jyothi	18EE32	Mrs. P Sumalatha		PS	18EE35			
MRS	Mr. M R Sidappaji	18EE33	Mrs. Reshma T M		RTM	18EE36			
PBB	Mr. Prashanth B B					18CPC39			
Prepared by: Dr H L Suresh									

Prepared by: Dr H L Suresh
 Designation: Prof Professor and Hod, EEE Department
 Signature: 

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SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY
 Krishnadevarayanagar, Hunsarasanahalli
 17th Mile, Bangalore - 562 157

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