



SIR M VISVESVARAYA INSTITUTE OF TECHNOLOGY

(Approved by AICTE New Delhi, Affiliated to VTU, Belagavi, ISO 9001:2008 Certified)
Off International Airport Road, Krishnadevaraya Nagar, Bengaluru – 562157

Department of Electrical & Electronics Engineering

Ref.:/VIT/DEEE/ /2020-21

Date: 03/10/2020

TEST-1 TIME TABLE (ON-LINE)

DAY- 1			Test Date: 12/10/2020	
Session-1 10.00 - 11.30 AM			Session-2 2.0 -3.30 PM	
Semester	Sub Code	Subject	Sub Code	Subject
3	18MAT31	Transform Calculus, Fourier Series and Numerical Techniques	18EE32	Electrical Circuit Analysis
5	18 EE51	Management and Entrepreneurship	18 EE52	Microcontroller
	15EE51/17EE51	Management and Entrepreneurship	15EE52 /17EE52	Micro Controller
DAY- 2			Test Date: 13/10/2020	
3	18EE33	Transformers and Generators	18 EE 34	Analog Electronic Circuits
5	18 EE53	Power Electronics	18 EE54	Signals and Systems
	15EE53 /17EE53	Power Electronics	15EE54/17EE54	Signals and Systems
DAY- 3			Test Date: 14/10/2020	
3	18 EE 35	Digital System Design	18 EE 36	Electrical and Electronic Measurements
5	18 EE55	Electrical Machine Design	18 EE56	High Voltage Engineering
	15EE553 /17EE553	Estimating and Costing	15EE563 /17EE563	Renewable Energy Systems



HOD 03/10/2020

DEPT. OF ELECTRICAL ENGG.

SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY
Krishnadevarayanagar, Hunasamaranahalli
(Via) Yelahanka Bangalore-562 157



PRINCIPAL

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Note: Due Placement Interview on 12/10/2020 to 13/10/2020, for 7th Semester
Test-1 rescheduled as follows

DAY- 1			Test Date: 08/10/2020	
Session-1 10.00-1130 AM			Session-2 2.0 -3.30 PM	
Semester	Sub Code	Subject	Sub Code	Subject
7	17EE71	Power System Analysis – 2	17EE72	Power System Protection
DAY- 2			Test Date: 09/10/2020	
7	17EE73	High Voltage Engineering	17EE742	Utilization of Electrical Power
			17EE744	Power System Planning
DAY- 3			Test Date: 10/10/2020	
7	17EE752	Testing and Commissioning of Power System Apparatus		

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HOD 03/10/2020
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*HOD EEE
to share this
time-table with all
students immediately*

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Sir M.V.I.T., Bangalore-562 157.