



SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY, BANGALORE - 562 157

RECORD FORMATS (ISO 9001:2005)

R/PT/04/02: Rev. 01

DEPARTMENT: ELECTRICAL AND ELECTRONICS ENGINEERING
SEM: III B

III SEMESTER B SECTION TIME TABLE WITH EFFECT FROM 01/09/2020
(ON-LINE)

2019-20 ODD semester

DAY	09:30 AM TO 10:30 AM	11:00 AM TO 12:00 PM	01:00 PM TO 02:00 PM	02:30 PM TO 03:30 PM	04:00 PM TO 05:00 PM
MON	Math-III	EEM(RTM)	DSD(PS)	T&G(MRS)	
TUE	EEM(RTM)	DSD(PS)	Math-III	AEC(VNA)	Constitution of India, Professional Ethics and Cyber Law
WED	ECA(PJ)	AEC(VNA)	EEM(RTM)	T&G(MRS)	
THR	AEC(VNA)	T&G(MRS)	ECA(PJ)	Math-III	
FRI	DSD(PS)	ECA(PJ)	AEC(VNA)	EEM(RTM)	
SAT	T&G(MRS)	Math-III	ECA(PJ)	DSD(PS)	

18MAT31	Engineering Mathematics -III	18EE35	Digital System Design(DSD)
18EE32	Electrical Circuit Analysis(ECA)	18EE36	Electrical and Electronics Measurements(EEM)
18EE33	Transformers and Generators(T&G)	18EE37	Electrical Machines Laboratory - I
18EE34	Analog Electronics circuits(AEC)	18EE38	Electronics Laboratory
18PC39	Constitution of India, Professional Ethics and Cyber Law		

STAFF NAMES

YHG	Dr. Gangadhariah Y H	18MAT31	Mr. Vidyaashree N A	VNA	18EE34
PJ	Ms. P Jyothi	18EE32	Mrs. P Sumalatha	PS	18EE35
MRS	Mr. M R Sidappai	18EE33	Mrs. Reshma T M	RTM	18EE36
PBB	Mr. Prashanth B B				18PC39

Prepared by: Dr H.L.Suresh
Designation: Professor and HOD, EEE Department
Signature:

Approved by: Dr. V R Manjunath
Designation: Principal
Signature:

PROF. & HEAD

DEPT. OF ELECTRICAL ENGG.

SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY
Krishnadevarayanagar, Hunsarinarahalli
(Vidya) Yelahanka Bangalore-562 157

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SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY
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R/P/04/02: Rev. 01

RECORD FORMATS (ISO 9001:2008)

DEPARTMENT: ELECTRICAL AND ELECTRONICS ENGINEERING

III SEMESTER A SECTION TIME TABLE WITH EFFECT FROM 01/09/2020
[ON-LINE]

2019-20 ODD semester

DAY	09:30 AM TO 10:30 AM	11:00 AM TO 12:00 PM	01:00 PM TO 02:00 PM	02:30 PM TO 03:30 PM	04:00 PM TO 05:00 PM
MON	Math-III	EEM(JBB)	DSD(RRK)	T&G(NMS)	
TUE	ECA (MSS)	DSD(RRK)	Math-III	AEC(KSR)	
WED	T&G(NMS)	AEC(KSR)	EEM(JBB)	ECA (MSS)	Constitution of India, Professional Ethics and Cyber Law
THR	AEC(KSR)	T&G(NMS)	ECA (MSS)	Math-III	
FRI	DSD(RRK)	ECA (MSS)	AEC(KSR)	EEM(JBB)	
SAT	EEM(JBB)	Math-III	T&G(NMS)	DSD(RRK)	
18MAT31 Engineering Mathematics -III					
18EE32	Electrical Circuit Analysis(ECA)	18EE35	Digital System Design(DSD)		
18EE33	Transformers and Generators(T&G)	18EE36	Electrical and Electronics Measurements(EEM)		
18EE34	Analog Electronics circuits(AEC)	18EE37	Electrical Machines Laboratory - I		
18CPC39	Constitution of India, Professional Ethics and Cyber Law	18EE38	Electronics Laboratory		
STATE NAMES					
CMD	Mrs.Chethana M. D.	18MAT31	Mr. Kumar Swamy	18EE34	
MSS	Dr. M S Suresh	18EE32	Mrs. Rekha Radhakrishnan	18EE35	
NMS	Mrs. Nanda Shivmogi	18EE33	Mrs. Jani B Borad	18EE36	
PBB	Mr. Prashanth B B			18CPC39	

Prepared by: Dr H.L.Suresh

Designation: Professor and HOD, EEE Department
Signature:

Approved by: Dr. V.R. Manjunath

Designation: Principal
Signature:

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RECORD FORMATS (ISO 9001:2008)

R/PP/04/02: Rev. 01

DEPARTMENT: ELECTRICAL AND ELECTRONICS ENGINEERING
SEM: VII B

VII SEMESTER B SECTION TIME TABLE
WITH EFFECT FROM 01/09/2020 [Con Line 5]

2019-20 ODD semester

DAY	09.30 AM TO 10:30 AM	11:00 AM TO 12.00 PM	01:00 PM TO 02:00PM	02:30 PM TO 03.30 PM	
MON	PSA-2(PS)	PS(BT)	PSP(MSS)	HVE(AC)	
TUE	PS(BT)	PSP(MSS)	HVE(AC)	UEP(RTM)	
WED	UEP(RTM)	PSA-2(PS)	PS(BT)	T&C(BC)	
THR	HVE(AC)	UEP(RTM)	PSA-2(PS)	T&C(BC)	
FRI	PSP(MSS)	HVE(AC)	T&C(BC)	PS(BT)	
SAT	T&C(BC)	PSA-2(PS)	UEP(RTM)	PSP(MSS)	
17EE71	Power system Analysis - 2 (PSA-2)		17EE752	Testing and Commissioning of Power System Apparatus(T&C)	
17EE72	Power system Protection(PS)		17EE76	Power system Simulation Laboratory	
17EE73	High voltage Engineering(HVE)		17EE77	Relay and High Voltage Lab	
17EE742	Utilization of Electrical Power(UEP)		17EE78	Project Phase I+ project work Seminar	
17EE744	Power system Planning(PSP)				
Staff Names					
PS	Mrs. Sumalatha P	17EE71	Mr Bhaskar C	BC	17EE752
BT	Mrs. Bindhya Tayagi	17EE72	Dr. M S Suresh	MSS	17EE744
AC	Mrs. Anchal	17EE73	Mrs. Reshma T M	RTM	17EE742

Prepared by: Dr H.L. Suresh

Designation: Professor and Hbd, EEE Department

Signature:

Approved by: Dr. V R Manjunath

Designation: Principal

Signature:

DEPT. OF ELECTRICAL ENGG

SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY
Krishnadevarayanagar, Hunasamaranahalli

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SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY
Krishnadevarayanagar - 562 157



SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY, BANGALORE - 562 157

RECORD FORMATS (ISO 9001:2008)

R/P/04/03: Rev. 01

V SEMESTER A SECTION TIME TABLE
WITH EFFECT FROM 01/09/2020 [on-line]

DEPARTMENT: ELECTRICAL AND ELECTRONICS ENGINEERING

2019-20 ODD semester

SEM: V A

DAY	09.30 AM TO 10:30 AM	11:00 AM TO 12.00 PM	01:00 PM TO 02:00PM	02:30 PM TO 03.30 PM
MON	MP(RS)	M&E(AC)	PE(BT)	EMD(DB)
TUE	M&E(AC)	MP(RS)	HVE(CVM)	S&S(HLS)
WED	PE(BT)	HVE(CVM)	M&E(AC)	MP(RS)
THR	EMD(DB)	PE(BT)	S&S(HLS)	HVE(CVM)
FRI	S&S(HLS)	EMD(DB)	MP(RS)	M&E(AC)
SAT	HVE(CVM)	S&S(HLS)	EMD(DB)	PE(BT)
18EE51	Management and Entrepreneurship(M&E)		18EE56	High Voltage Engineering(HVE)
18EE52	Micro Controller(MP)		18EE57	Micro Controller Laboratory
18EE53	Power Electronics(PE)		18EE58	Power Electronics Laboratory
18EE54	Signal and Systems(S&S)		18CIV59	Environmental Studies
18EE55	Electrical Machine Design(EMD)			
STAFF NAMES				
AC	Mrs.Anchal	18EE51	Dr. H L Suresh	HLS
RS	Dr. R Subha	18EE52	Mrs D Beula	DB
BT	Mrs. Bindhya Tayagi	18EE53	Dr. C V Mohan	CVM
Prepared by: Dr H.L.Suresh				
Designation: Professor and Head, Department of Electrical Engg				
Signature: 				
Approved by: Dr. V R Ramesh				
Designation: Principal				
Signature: 				

SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY

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(Vice) Velankar Bangalore-562 157

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Krishnadevarayanagar, Hunsuramangaluru



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RECORD FORMATS (ISO 9001:2008)

VII SEMESTER A SECTION TIME TABLE
WITH EFFECT FROM 01/09/2020 (on-line)

R/PP/04/02: Rev. 01

DEPARTMENT: ELECTRICAL AND ELECTRONICS ENGINEERING
SEM: VII A

2019-20 ODD semester

DAY	09.30 AM TO 10:30 AM	11:00 AM TO 12.00 PM	01:00 PM TO 02:00PM	02:30 PM TO 03.30 PM
MON	PSA-2(DB)	PS(RSP)	PSP(MSS)	UEP(JBB)
TUE	UEP(JBB)	PSP(MSS)	T&C(NKR)	HVE(KBV)
WED	T&C(NKR)	PSA-2(DB)	PS(RSP)	HVE(KBV)
THR	HVE(KBV)	UEP(JBB)	PSA-2(DB)	T&C(NKR)
FRI	PSP(MSS)	T&C(NKR)	HVE(KBV)	PS(RSP)
SAT	PS(RSP)	PSA-2(DB)	UEP(JBB)	PSP(MSS)

17EE71	Power system Analysis - 2 (PSA-2)	17EE752	Testing and Commissioning of Power System Apparatus(T&C)
17EE72	Power system Protection(PS)	17EE76	Power system Simulation Laboratory
17EE73	High voltage Engineering(HVE)	17EE77	Relay and High Voltage Lab
17EE742	Utilization of Electrical Power(UEP)	17EE78	Project Phase I+ project work Seminar
17EE744	Power system Planning(PSP)		

Staff Names

DB	Mrs.D Beula	17EE71	JBB	Mrs.Janki B Borad	17EE742
RSP	Mr. Sivapriyan R	17EE72	MSS	Dr. M S Suresh	17EE744
KBV	Mr. Kiran B V	17EE73	NKR	Mr. Nitinkumar Reddy	17EE752

Prepared by: Dr H.L.Suresh

Approved by: Dr. V R Manjunath

Designation: | Professor and Hod, EEE Department

Designation: Principal

Signature:

Signature:

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SEM: V B

2019-20 ODD semester

V SEMESTER B SECTION TIME TABLE
WITH EFFECT FROM 01/09/2020 [ON-LINE]

DAY	09.30 AM TO 10.30 AM	11:00 AM TO 12.00 PM	01:00 PM TO 02:00PM	02:30 PM TO 03.30 PM
MON	MP(RRK)	HVE(BC)	M&E(NKR)	S&S(RS)
TUE	HVE(BC)	EMD(RSP)	PE(MPV)	M&E(NKR)
WED	S&S(RS)	MP(RRK)	HVE(BC)	EMD(RSP)
THR	EMD(RSP)	S&S(RS)	MP(RRK)	PE(MPV)
FRI	PE(MPV)	MP(RRK)	EMD(RSP)	M&E(NKR)
SAT	M&E(NKR)	PE(MPV)	S&S(RS)	HVE(BC)

18EE51	Management and Entrepreneurship(M&E)	18EE56	High Voltage Engineering(HVE)
18EE52	Micro Controller(MP)	18EE57	Micro Controller Laboratory
18EE53	Power Electronics(PE)	18EE58	Power Electronics Laboratory
18EE54	Signal and Systems(S&S)	18CI59	Environmental Studies
18EE55	Electrical Machine Design(EMD)		

STAFF NAMES

NKR	Mr Nitin Kumar Reddy	18EE51	Dr. Subha R	RS	18EE54
RRK	Rekha Radhakrishnan	18EE52	Mr. Sivapriyan R	RSP	18EE35
MPV	Mrs. M P Vasanthi	18EE53	Mr. Bhaskar C	BC	18EE36

Prepared by: Dr H.L.Suresh

Approved by: Dr. V R Manjunath

Designation: Professor and Mod, EEE Department

Designation: Principal

Signature:

Signature:

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